

Additional Digital Output information for the SDG7000A arbitrary function generator.

November 03, 2022

1. The LVTTTL voltage is 3.3V and the rise/fall time is 1 ns.

The common mode voltage of LVDS is 1.25 V (typical) and the differential mode is 350 mV (typical). The rise/fall time is 400 ps.

2. The digital and analog channels are not synchronous and they are independent.



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