

## **Revision History**

Date	Version	Revision	vision	
2/1/2018	5.01.01.15R5	1. Optimized PLL configur	Optimized PLL configuration Fixed several bugs	
		2. Fixed several bugs		
		a) Enabling Mod on	CH2 may disable Burst on CH1	
		b) Duty cycle error or	n Square wave with very long period	
		c) SCPI command c	cannot adjust the phase difference	
		between two syncl	nronized units	
		d) Manual trigger	cannot trigger both channels	
		simultaneously		
11/11/2015	5.01.01.15R1	1. Supported bandwidth up	Supported bandwidth update option (Note: The option will	
		be only available on the	be only available on the hardware after Jan. 1st, 2016)	
		2. Added channel coupling	Added channel coupling function	
		3. Fixed miscellaneous mir	Fixed miscellaneous minor bugs	